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OUR REF: 2003\_1690A/JRF/00177  
Applicant: Koichi HIRANO et al.  
Serial No.: NEW  
Title: CAPACITOR AND METHOD FOR PRODUCING THE SAME, AND CIRCUIT BOARD  
WITH A BUILT-IN CAPACITOR AND METHOD FOR PRODUCING THE SAME

Due Date: December 27, 2003

Filing Date: December 4, 2003

Receipt of the following papers is acknowledged:

1. Utility Patent Application Transmittal (37 CFR 1.53(b))
2. Patent Office Fee Transmittal Form [in duplicate]
3. Specification comprising 124 pages, including claims and abstract
4. 11 sheets drawings (A4P, Figs. 1-16)
5. Declaration and Power of Attorney [executed]
6. Recordation Form Cover Sheet, and Assignment
7. Information Disclosure Statement, Form PTO-1449, 2 references
8. Claim of Priority Under 35 USC 119, and certified copy of Japanese Patent Application No. 2002-379231
9. Check in the amount of \$1970.00

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Date: December 4, 2003

Attorney: JRF/fs

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